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List of Patents and Publications
Cited by Applicant
(Use several sheets if necessary)

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Applicant:

Prasad N. Golla et al.

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Group:

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## OTHER DOCUMENTS Examiner Author, Title, Date, Pertinent Pages, Etc. Initial AA TIMA Lab.Research Reports; "Modeling and design of DN asynchronous priority arbiters for on-chip communication systems"; J.B. Rigaud, J. Quartana, L. Fesquet and M. Renaudin; 7 pages; 2001. AB RECEIVED SEP 2 0 2002 AC Technology Center 2600 AD ΑE AF AG EXAMINER: Duc Duong 8/17/06 DATE CONSIDERED: